



InP Substrate Products

Product Feature

- VGF grown crystals with high crystal lattice perfection, good electrical property and uniformity;
- Precise wafer orientation with high accuracy orientation flats;
- Clean surface with ultra low roughness prepared by unique CMP technique and cleaning process.

Electrical parameters

(一) Electrical Parameters					
Material	Un-InP	S-InP	Sn-InP	Fe-InP	Zn-InP
Conduction Type	N	N	N	Si	P
Inch	2/3/4	2/3/4	2/3/4	2/3/4	2/3/4
Carrier Concentration (cm ⁻³)	(0.3~2)E18	(1~8)E18	(1~4)E18	-	(0.6~2)E18 (3~6)E18
Resistivity (Ω.cm)	-	(0.6~2.5)E-3	(1~6)E-3	≥1E7	-
Mobility (cm ² /V.s)	(3.5~4.5)E3	(1~2)E3	(1.2~2.1)E3	≥2E3	70~90 50~70
EPD (cm ⁻²)	2": ≤1000 3": ≤1000 4": ≤5000	2": ≤1000 3": ≤1000 4": ≤5000	2": ≤5000 3": ≤5000 4": ≤10000	2": ≤5000 3": ≤5000 4": ≤5000	2": ≤1000 3": ≤1000 4": ≤10000

Wafer processing parameters

(二) Wafer processing parameters			
Specification	2 inch	3 inch	4 inch
Orientation	(100)±0.3°	(100)±0.3°	(100)±0.3°
Diameter (mm)	50.0±0.3	76.0±0.3	100.0±0.3
Thickness (μm)	350±15	600±15	625±25
Primary Flat Length (mm)	16.0±1.0	22.0±1.0	32.5±1.0
Secondary Flat Length (mm)	7.0±1.0	12.0±1.0	18.0±1.0
Flatness	TTV (μm)	< 10	< 15
	Bow (μm)	< 10	< 15
	Warp (μm)	< 15	< 15
PE/PP , EPI-READY ; Special specification wafers can be supplied according to customer requirements.			

Special specification products can be processed according to the requirements of users.

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